



Complementary High Density Trench MOSFET

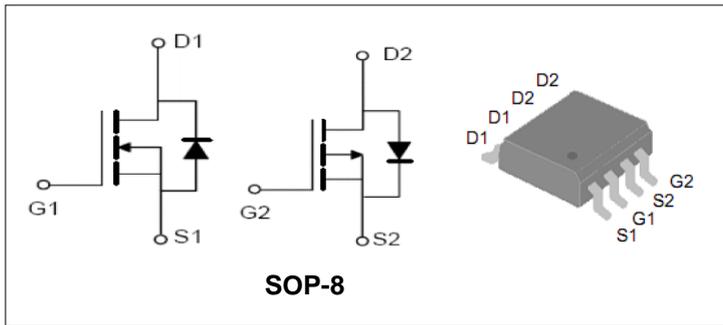
Features

- Improved dv/dt Capability, High Ruggedness.
- Maximum Junction Temperature Range (150°C)

Applications

- D C Fan
- Brushless motor
- Optimized for Power Management Applications for Portable Products, such as H-bridge, Inverters Car Charger and Others

N-Channel		
BVDSS	60	V
ID	6	A
RDSON@VGS=10V	29	mΩ
RDSON@VGS=4.5V	33	mΩ



P-Channel		
BVDSS	-60	V
ID	-5	A
RDSON@VGS=-10V	68	mΩ
RDSON@VGS=-4.5V	87	mΩ

Order Information

Product	Package	Marking	Reel Size	Reel	Carton
PTS05C06	SOP-8	PTS05C06	13inch	3000PCS	48000PCS

Absolute Maximum Ratings

Symbol	Parameter		N-Channel	P-Channel	Unit
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage		60	-60	V
$V_{GS}$	Gate-Source Voltage		±20	±20	V
$T_J$	Maximum Junction Temperature		150	150	°C
$T_{STG}$	Storage Temperature Range		-55 to 150	-55 to 150	°C
$I_S$	Diode Continuous Forward Current	$T_A = 25^\circ C$	6	-5	A
<b>Mounted on Large Heat Sink</b>					
$I_{DM}$	Pulse Drain Current Tested (Silicon Limit) (Note1)	$T_A = 25^\circ C$	24	-20	A
$I_D$	Continuous Drain current	$T_A = 25^\circ C$	6	-6	A
$P_D$	Maximum Power Dissipation	$T_A = 25^\circ C$	2		W
$R_{\theta JA}$	Thermal Resistance Junction-to-Ambient (Note2)		62.5		°C/W



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## N-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain- Source Breakdown Voltage	VGS=0V ID=250μA	60	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain current	VDS=30V,VGS=0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	VDS=VGS,ID=250μA	1	1.6	2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance (Note3)	VGS=10V, ID=6A	--	29	35	mΩ
		VGS=4.5V, ID=5A	--	33	38	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated) (Note4)</b>						
C <sub>iss</sub>	Input Capacitance	VDS= 30V, VGS=0V, F=1MHz	--	1310	--	pF
C <sub>oss</sub>	Output Capacitance		--	59.4	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	44.3	--	pF
Q <sub>g</sub>	Total Gate Charge	VDS= 30V, ID= 6A, VGS= 10V	--	31	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	6	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	8	--	nC
<b>Switching Characteristics (Note4)</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	VDD=30V, ID=6A, VGEN=10V, RG=3Ω	--	6	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	11	--	nS
t <sub>d(off)</sub>	Turn-off Delay Time		--	19	--	nS
t <sub>f</sub>	Turn-off Fall Time		--	5	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage (Note3)	IS=5A,VGS=0V	--	--	1.2	V



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## P-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain- Source Breakdown Voltage	VGS=0V ID=-250μA	-60	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain current	VDS=-60V,VGS=0V	--	--	-1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.0	-2	-2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance (Note3)	VGS=-10V, ID=-5A	--	68	75	mΩ
		VGS=-4.5V, ID=-4A	--	88	95	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated) (Note4)</b>						
C <sub>iss</sub>	Input Capacitance	VDS= -30V, VGS=0V, F=1MHz	--	1210	--	pF
C <sub>oss</sub>	Output Capacitance		--	779	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	387	--	pF
Q <sub>g</sub>	Total Gate Charge	VDS= -30V, ID= -5A, VGS= -10V	--	26	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	5.1	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	7.9	--	nC
<b>Switching Characteristics (Note4)</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	VDD=- 30V, ID=-5A, VGEN=-10V, RG=3Ω	--	8	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	10.1	--	nS
t <sub>d(off)</sub>	Turn-off Delay Time		--	59	--	nS
t <sub>f</sub>	Turn-off Fall Time		--	27	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage (Note3)	IS=-5A,VGS=0V	--	-0.8	-1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec
3. Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
4. Guaranteed by design, not subject to production testing.



Complementary High Density Trench MOSFET

Typical Characteristics(N-Channel)

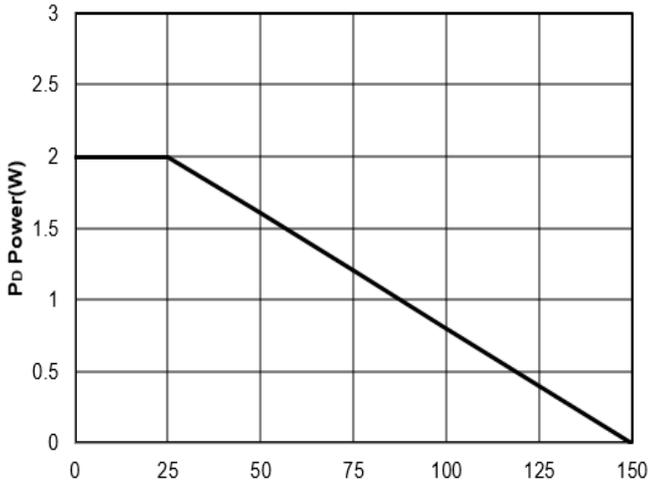


Figure1: Tj Junction Temperature (°C)

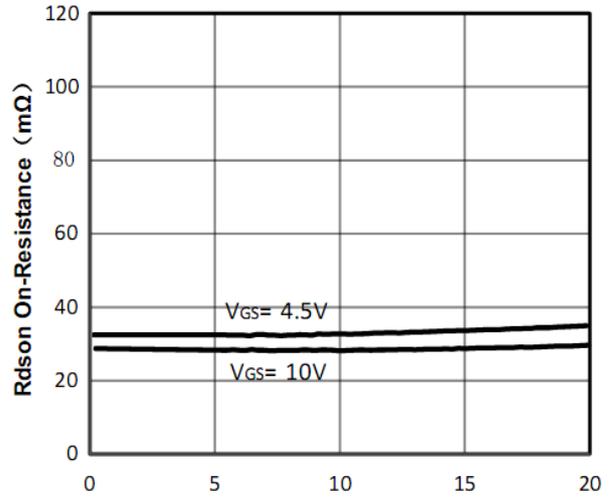


Figure2: Id Drain Current (A)

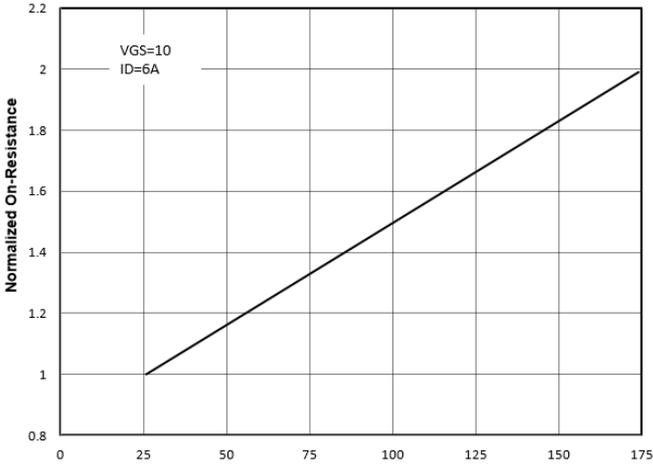


Figure3: Tj Junction Temperature (°C)

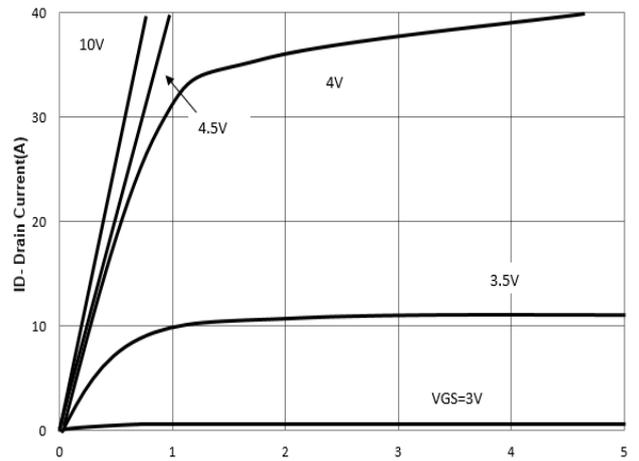


Figure4: Vds Drain-Source Voltage (V)

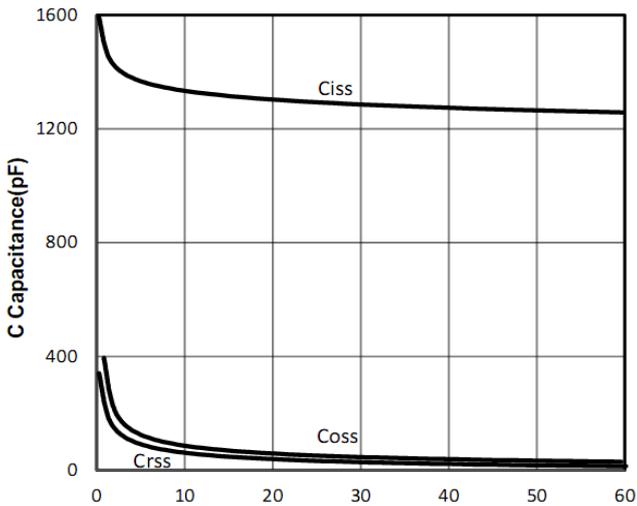


Figure5: Vds Drain-Source Voltage (V)

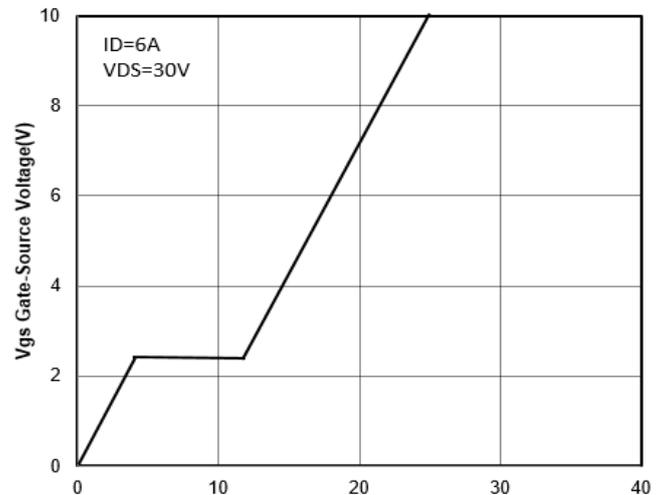


Figure6: Qg Gate Charge (nC)



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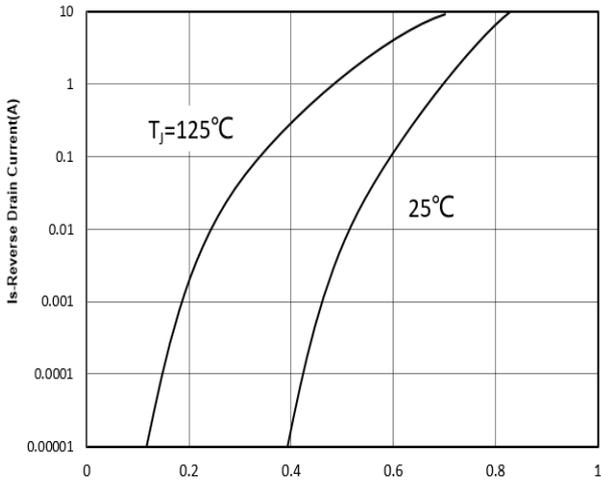


Figure7: Vsd Source-Drain Voltage (V)

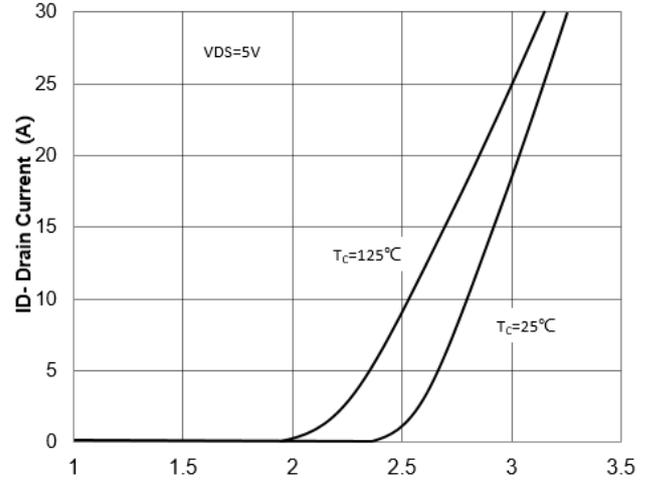


Figure8: Vgs Gate-Source Voltage (V)

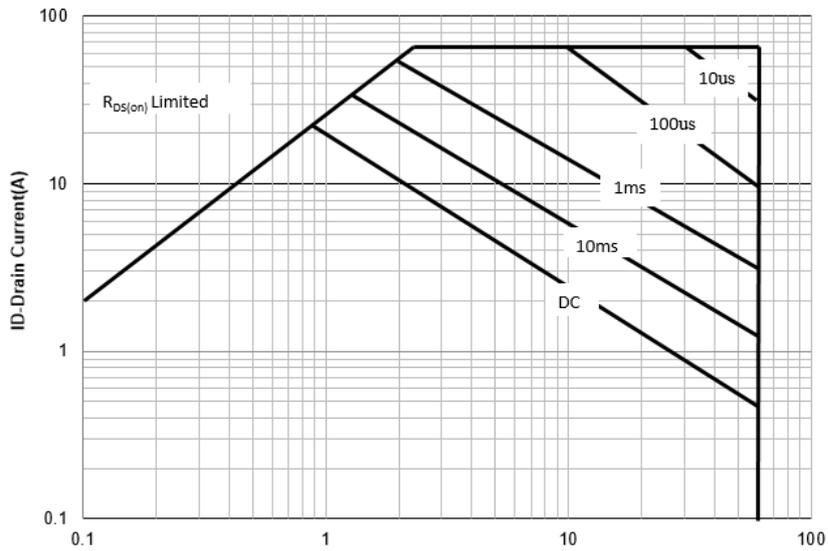


Figure9: Vsd Drain -Source Voltage (V)

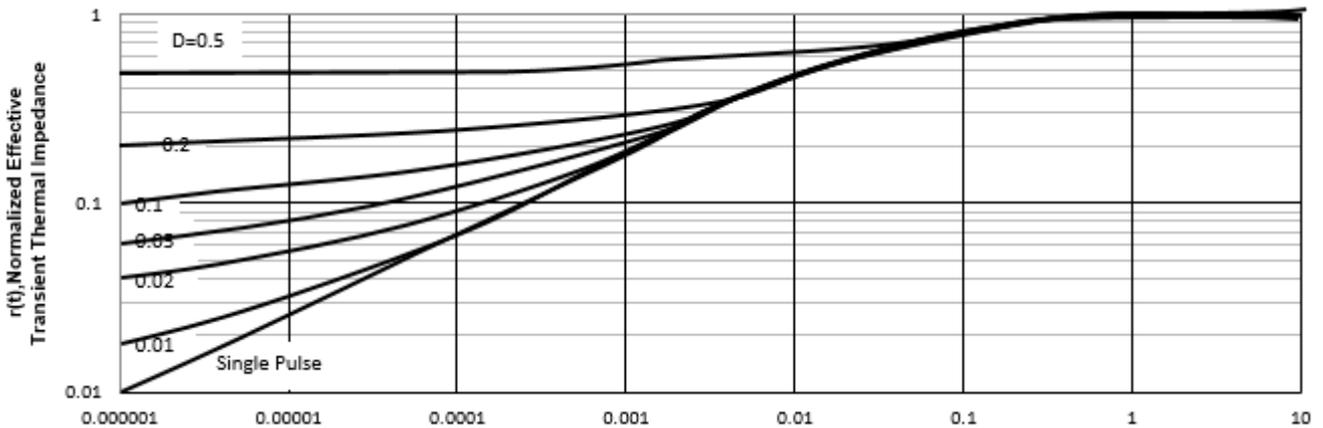


Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform(P-Channel):

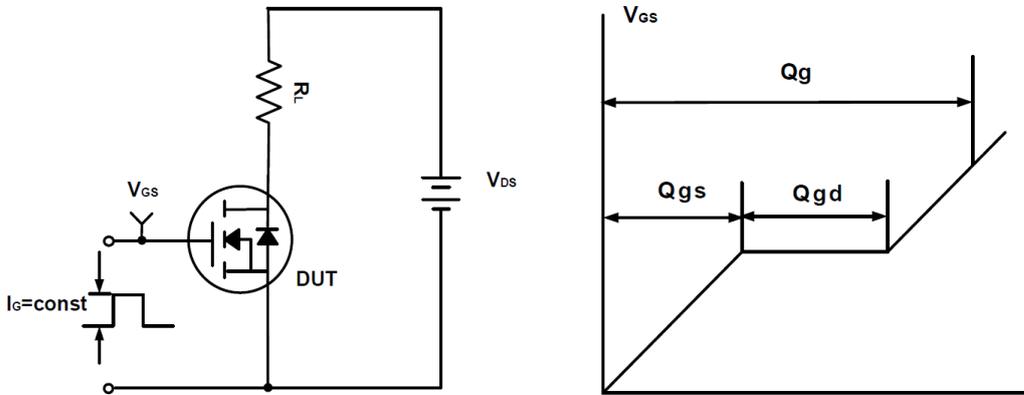


Figure A Gate Charge Test Circuit & Waveforms

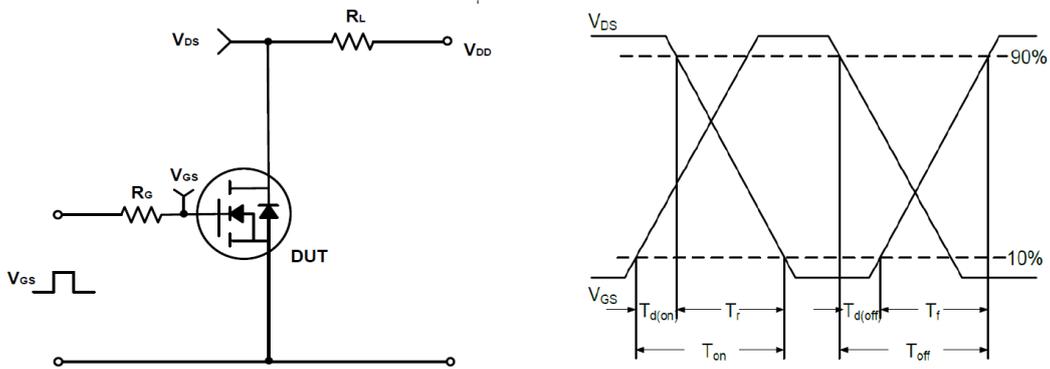


Figure B Switching Test Circuit & Waveforms

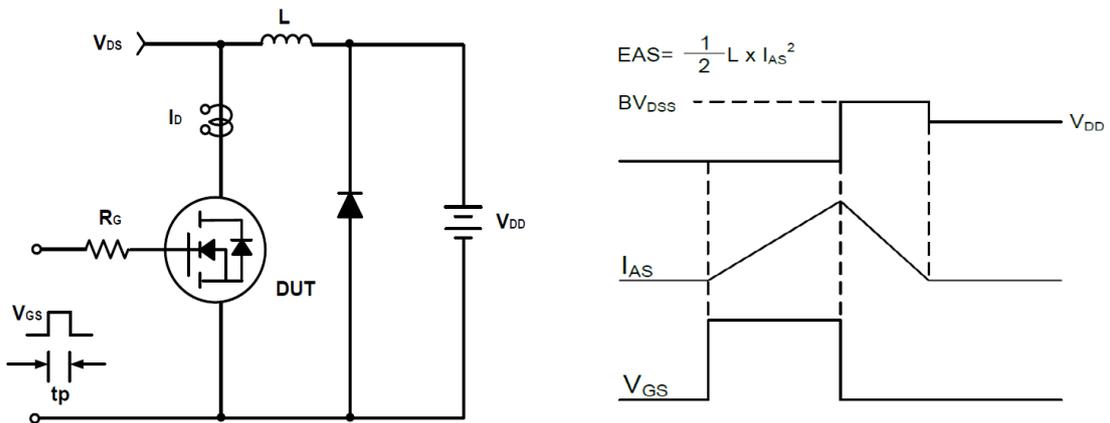


Figure C Unclamped Inductive Switching Circuit & Waveforms



Complementary High Density Trench MOSFET

Typical Characteristics(P-Channel)

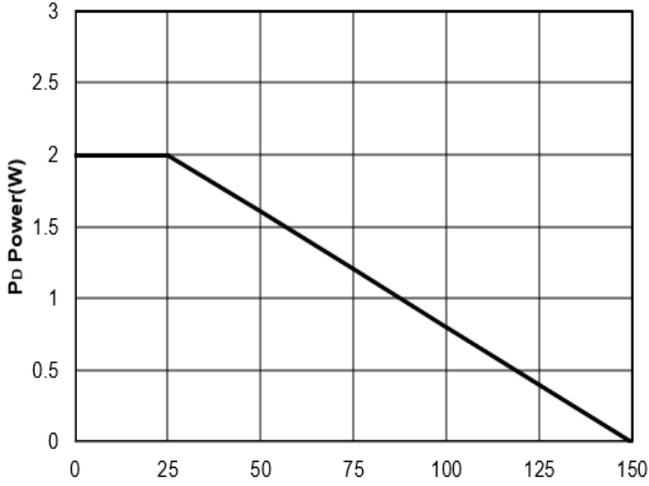


Figure1: T<sub>J</sub> Junction Temperature (°C)

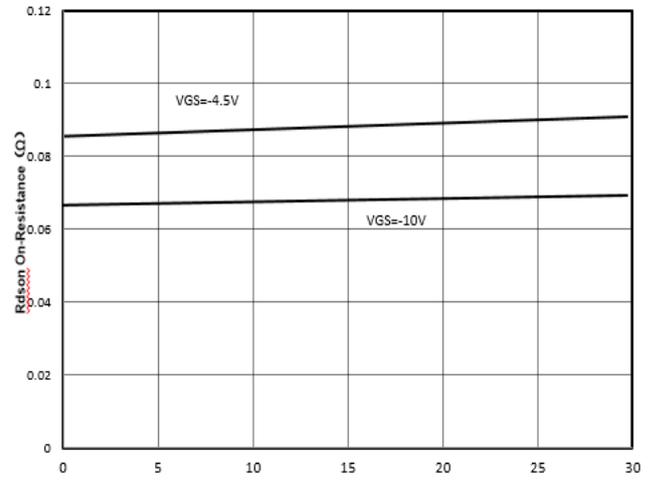


Figure2: -I<sub>D</sub> Drain Current (A)

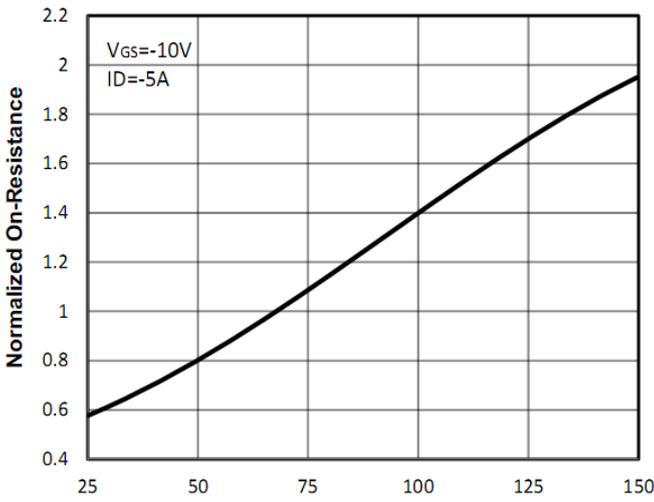


Figure3: T<sub>J</sub> Junction Temperature (°C)

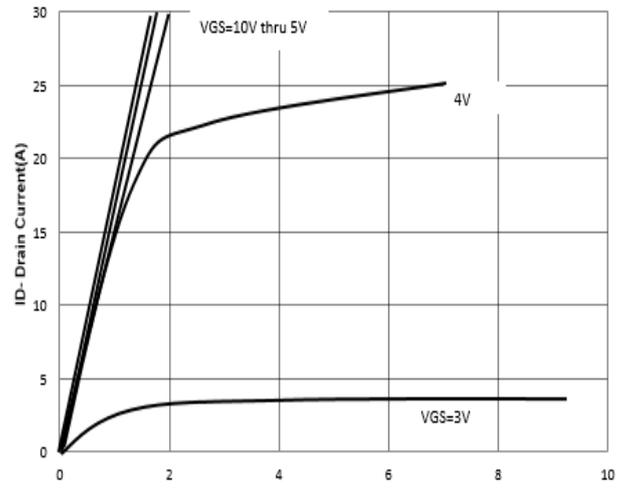


Figure4: -V<sub>DS</sub> Drain-Source Voltage (V)

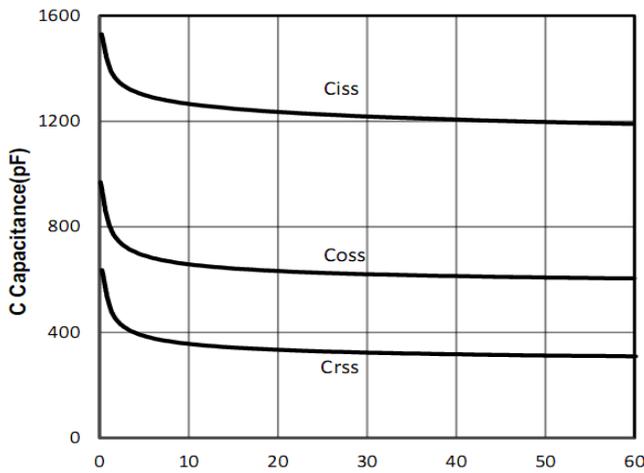


Figure5: -V<sub>DS</sub> Drain-Source Voltage (V)

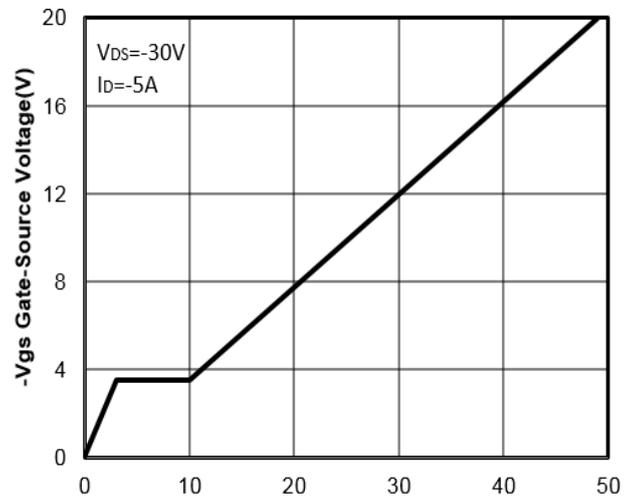


Figure6: Q<sub>g</sub> Gate Charge (nC)

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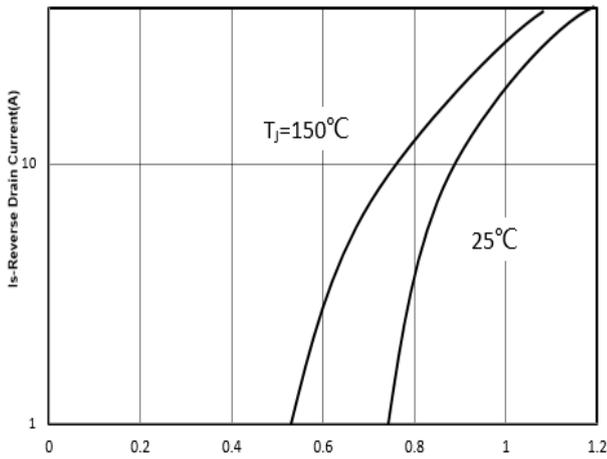


Figure7:  $-V_{sd}$  Source-Drain Voltage (V)

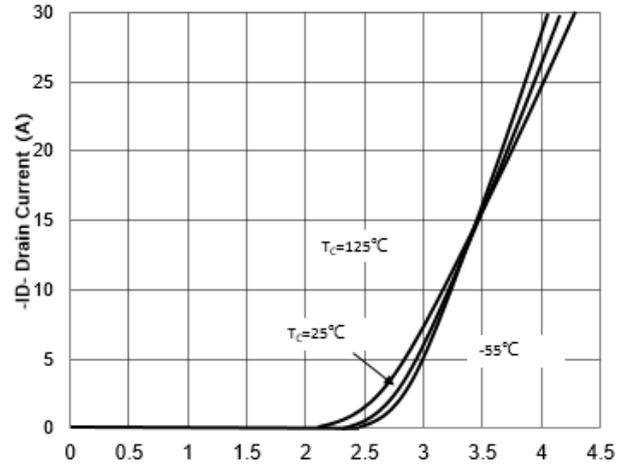


Figure8:  $-V_{gs}$  Gate-Source Voltage (V)

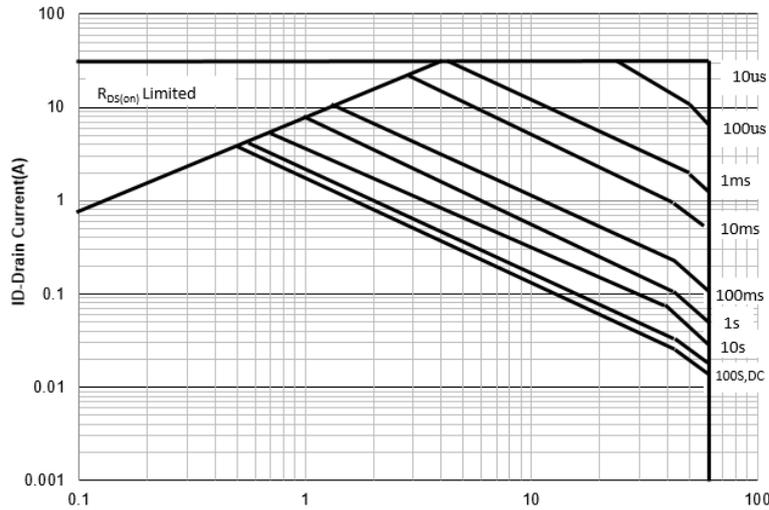


Figure9:  $-V_{ds}$  Drain -Source Voltage (V)

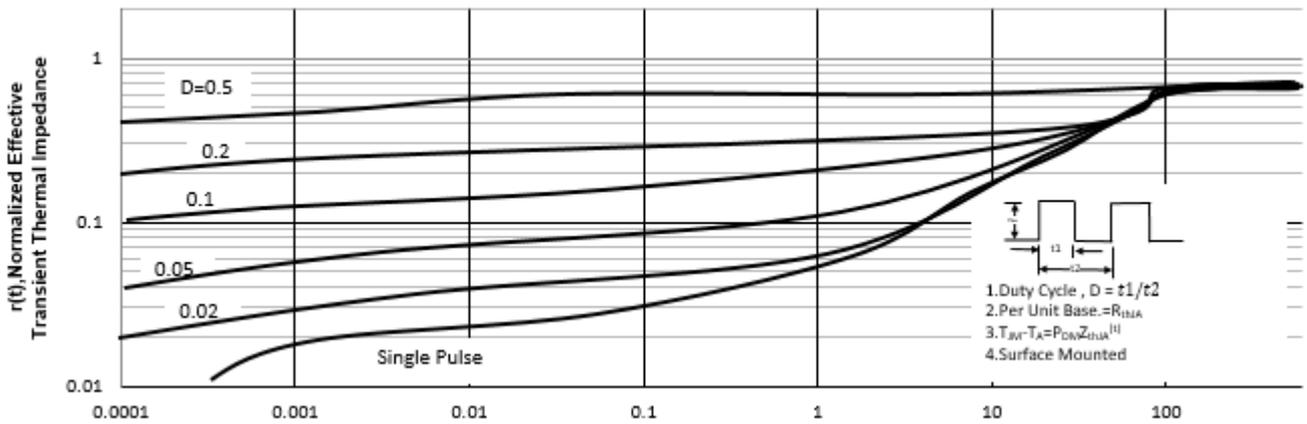


Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform(P-Channel):

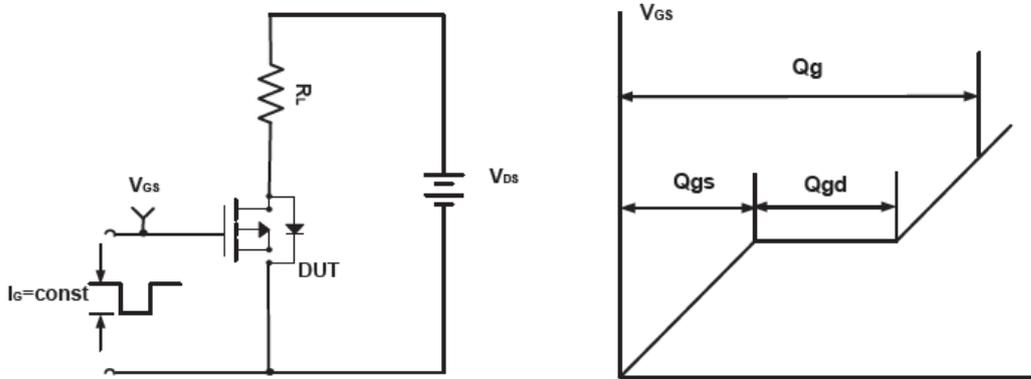


Figure D Gate Charge Test Circuit & Waveforms

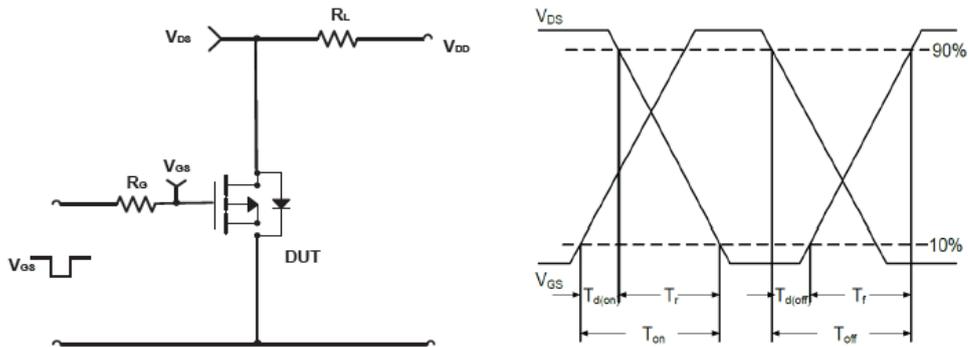


Figure E Switching Test Circuit & Waveforms

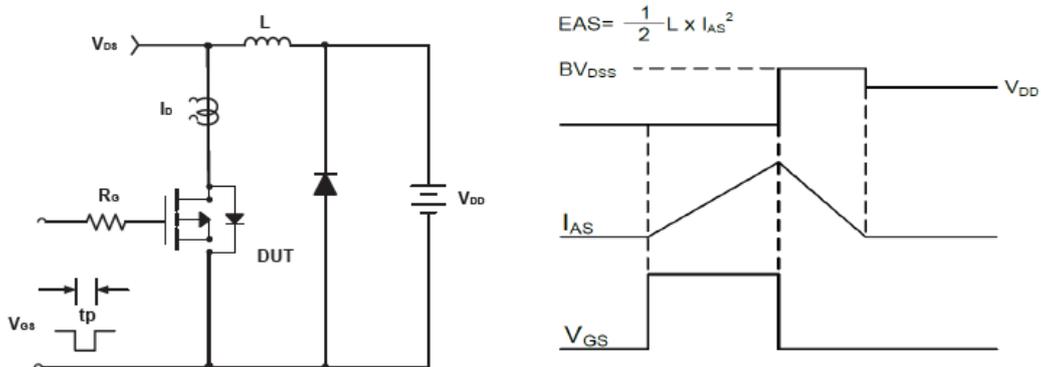
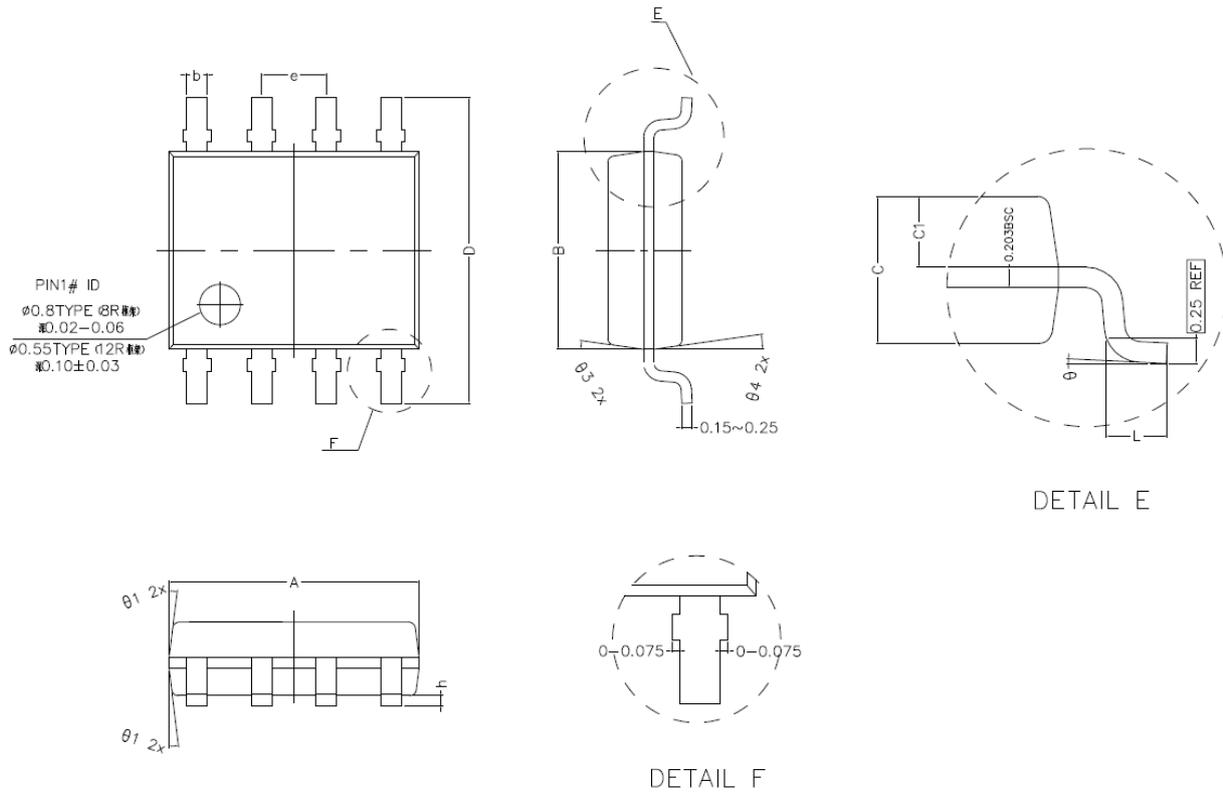


Figure F Unclamped Inductive Switching Circuit & Waveforms

**Complementary High Density Trench MOSFET**
**SOP-8 Package Outline Dimensions (Units: mm)**


COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	4.800	4.900	5.000
B	3.800	3.900	4.000
C	1.350	1.450	1.550
C1	0.650	0.700	0.750
D	5.900	6.100	6.300
L	0.500	0.600	0.700
b	0.350	0.400	0.450
h	0.050	0.150	0.250
e	1.270TYPE		
$\theta_1$	7° TYPE(8R)		12° TYPE(12R)
$\theta_2$	7° TYPE(8R)		10° TYPE(12R)
$\theta_3$	8° TYPE(8R)		12° TYPE(12R)
$\theta_4$	8° TYPE(8R)		10° TYPE(12R)
$\theta$	0° ~ 8°		